

DIM1200ESM33-F000

Single Switch IGBT Module

DS5831-1.0 JULY. 2005 (LN23824)

FEATURES

- 10µs Short Circuit Withstand
- Soft Punch Through Silicon
- Isolated MMC Base with AIN Substrates
- Lead Free construction
- High Thermal Cycling Capability

APPLICATIONS

- High Reliability Inverters
- Motor Controllers
- Traction Drives
- Choppers

The Powerline range of high power modules includes half bridge, chopper, dual, single and bidirectional switch configurations covering voltages from 600V to 3300V and currents up to 3600A.

The DIM1200ESM33-F000 is a single switch 3300V soft punch through, n channel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus full 10 μ s short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM1200ESM33-F000

Note: When ordering, please use the whole part number.

KEY PARAMETERS

V _{CES}		3300V
V _{CE (sat)} *	(typ)	2.8V
I _C	(max)	1200A
I _{C(PK)}	(max)	2400A

*(measured at the auxiliary terminals)

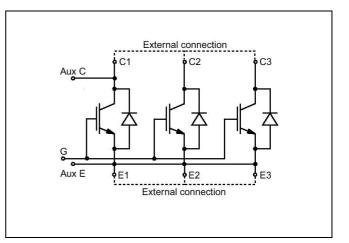


Fig. 1 Single switch circuit diagram

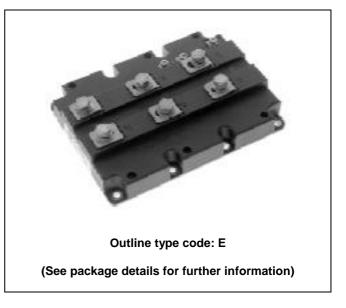


Fig. 2 Package

Caution: This device is sensitive to electrostatic discharge. Users should follow ESD handling procedures.



ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

Tcase = 25° C unless stated otherwise

Symbol	Parameter	Test Conditions		Units
V _{CES}	Collector-emitter voltage	$V_{GE} = 0V, T_j = -25^{\circ} C$	3300	V
V _{GES}	Gate-emitter voltage		±20	V
Ic	Continuous collector current	$T_{case} = 90^{\circ} C$	1200	А
I _{C(PK)}	Peak collector current	1ms, T _{case} =115° C	2400	А
P _{max}	Max. transistor power dissipation	$T_{case} = 25^{\circ} C, T_{j} = 150^{\circ} C$	15.6	kW
l ² t	Diode I ² t value (IGBT arm)	$V_R = 0, t_P = 10ms, T_{vj} = 125^{\circ} C$	720	kA ² S
Visol	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	6000	V
Q _{PD}	Partial discharge - per module	IEC1287. V ₁ = 3500V, V ₂ = 2600V, 50Hz RSM	10	рС





THERMAL AND MECHANICAL RATINGS

Internal insulation material:	AIN
Baseplate material:	AISiC
Creepage distance:	29mm
Clearance:	20mm
CTI (Critical Tracking Index):	175

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
R _{th(j-c)}	Thermal resistance – transistor (per switch)	Continuous dissipation – junction to case	-	-	8	° C/kW
R _{th(j-c)}	Thermal resistance – diode (per switch)	Continuous dissipation – junction to case	-	-	16	° C/kW
$R_{th(c-h)}$	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	-	6	° C/kW
Tj	Junction temperature	Transistor	-	-	150	°C
		Diode	-	-	125	°C
T _{stg}	Storage temperature range	-	-40	-	125	°C
-	Screw torque	Mounting – M6	-	-	5	Nm
		Electrical connections – M4	-	-	2	Nm
		Electrical connections – M8	-	-	10	Nm





ELECTRICAL CHARACTERISTICS

$T_{case} = 25^{\circ} C$ unless stated otherwise.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
ICES	Collector cut-off current	$V_{GE} = 0V, V_{CE} = V_{CES}$	-	-	5	mA
		$V_{GE} = 0V, V_{CE} = V_{CES}, T_{case} = 125^{\circ} C$	-	-	90	mA
I _{GES}	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$	-	-	1	μA
$V_{\text{GE(TH)}}$	Gate threshold voltage	I_{C} = 120mA, V_{GE} = V_{CE}	5.5	6.5	7.0	V
$V_{\text{CE}(\text{sat})^{\dagger}}$	Collector-emitter saturation voltage	V _{GE} = 15V, I _C = 1200A	-	2.8	-	V
		V_{GE} = 15V, I _C = 1200A, T _{case} = 125° C	-	3.6	-	V
I _F	Diode forward current	DC	-	1200	-	А
I _{FM}	Diode maximum forward current	t _p = 1ms	-	2400	-	А
V_{F^\dagger}	Diode forward voltage	I _F = 1200A	-	2.9	-	V
		I _F = 1200A, T _{case} = 125° C	-	3.0	-	V
Cies	Input capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$		216	-	nF
Cres	Reverse transfer capacitance	$V_{CE} = 25V$, $V_{GE} = 0V$, f = 1MHz		3.3		nF
L _M	Module inductance – per arm	-	-	10	-	nH
RINT	Internal resistance – per arm	-	-	0.09	-	mΩ
SC _{Data}	Short circuit. I _{sc}	$T_j = 125^{\circ} C, V_{cc} = 2500V,$ I ₁	-	6000	-	А
			-	5500	-	А
		IEC 60747-9				

Note:

 $\ensuremath{\underline{t}}$ Measured at the power busbars and not the auxiliary terminals

^{*}L is the circuit inductance + L_M



ELECTRICAL CHARACTERISTICS

T_{case} = 25° C unless stated otherwise.

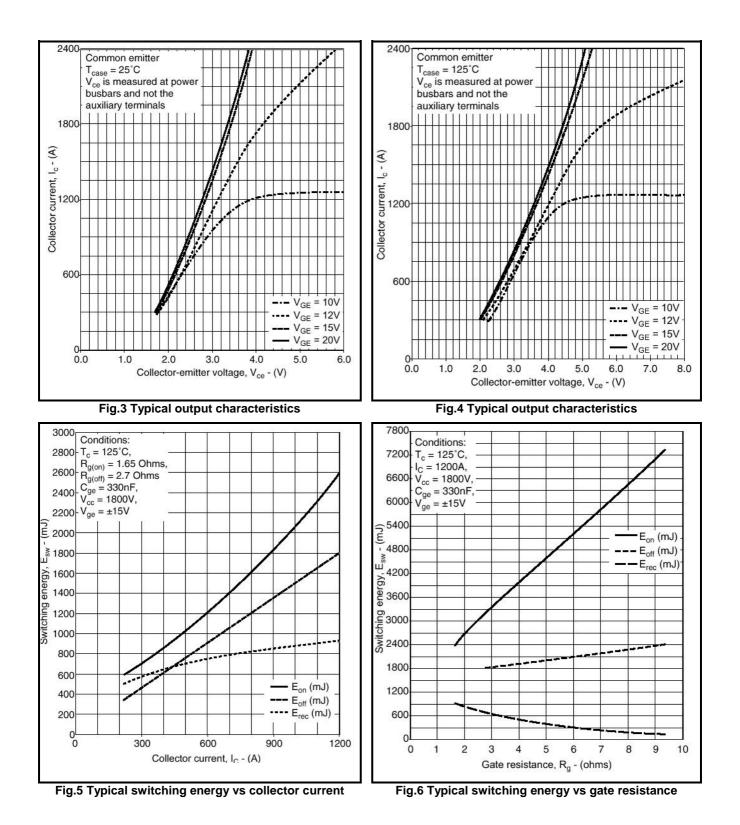
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
t _{d(off)}	Turn-off delay time	I _C = 1200A	-	2150	-	ns
t _f	Fall time	$V_{GE} = \pm 15V$	-	230	-	ns
E _{OFF}	Turn-off energy loss	V _{CE} = 1800V	-	1550	-	mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = R_{G(OFF)} = 2.7\Omega$	-	1300	-	ns
tr	Rise time	C _{ge} = 330nF	-	275	-	ns
Qg	Gate charge	L ~ 100nH	-	30	-	μC
Eon	Turn-on energy loss	I _C = 1200A, V _{GE} = ±15V, V _{CE} = 1800V,R _G = 1.65Ω, C _{ge} = 330nF, L~ 100nH	-	1850	-	mJ
Qrr	Diode reverse recovery charge	$I_F = 2400A, V_R = 1800V,$	-	480	-	μC
Irr	Diode reverse current	dl _F /dt = 6000A/µs	-	1000	-	А
E _{REC}	Diode reverse recovery energy		-	450	-	mJ

T_{case} = 125° C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
t _{d(off)}	Turn-off delay time	I _C = 1200A	-	2200	-	ns
t _f	Fall time	$V_{GE} = \pm 15V$	-	240	-	ns
EOFF	Turn-off energy loss	V _{CE} = 1800V	-	1800	-	mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = R_{G(OFF)} = 2.7\Omega$	-	1200	-	ns
tr	Rise time	C _{ge} = 330nF, L ~ 50nH	-	315	-	ns
Eon	Turn-on energy loss	I _C = 1200A, V _{GE} = ±15V, V _{CE} = 1800V,R _G = 1.65Ω, C _{ge} = 330nF, L~ 100nH	-	2600	-	mJ
Qrr	Diode reverse recovery charge	$I_F = 1200A, V_R = 1800V,$	-	900	-	μC
l _{rr}	Diode reverse current	dl _F /dt = 6000A/µs	-	1200	-	А
E _{REC}	Diode reverse recovery energy		-	900	-	mJ

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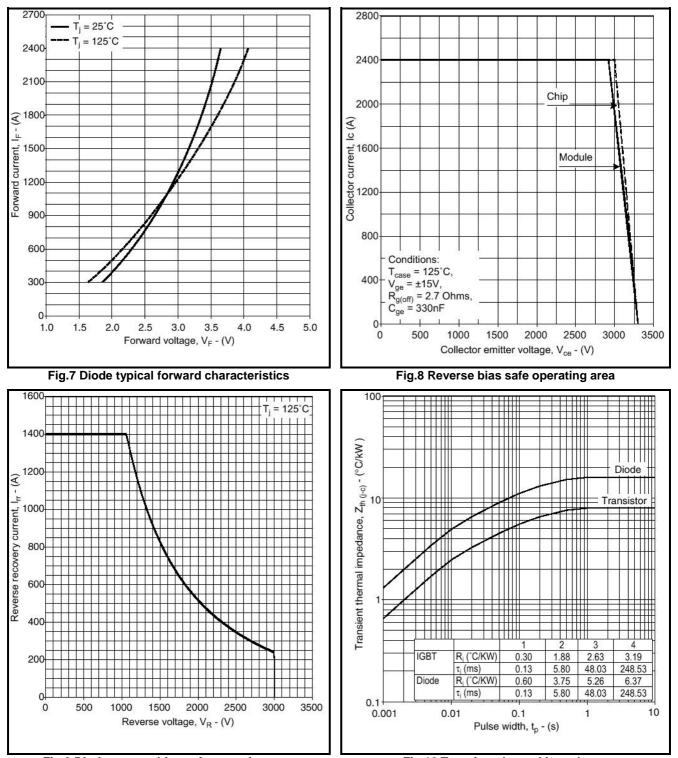


Fig.9 Diode reverse bias safe operating area

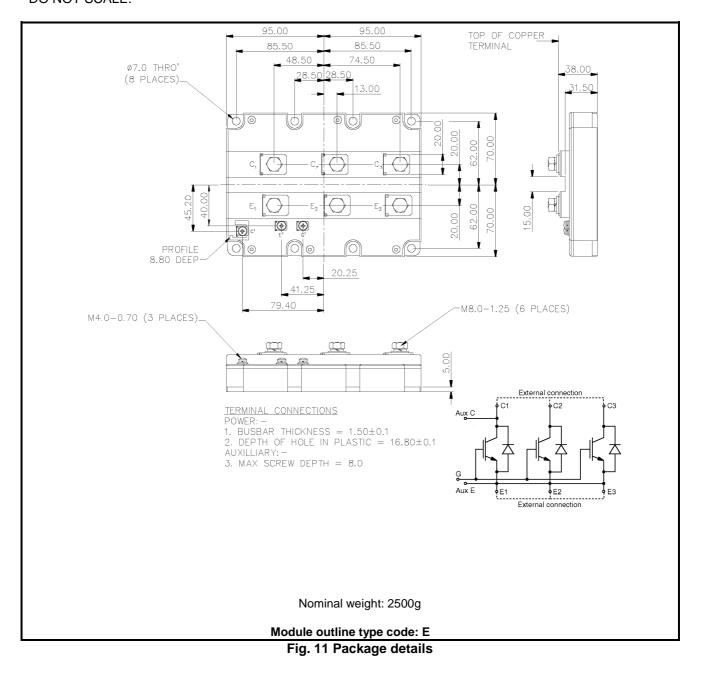
Fig.10 Transient thermal impedance

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PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.





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The Power Assembly group was set up to provide a support service for those customers requiring more than the basic semiconductor, and has developed a flexible range of heatsink and clamping systems in line with advances in device voltages and current capability of our semiconductors.

We offer an extensive range of air and liquid cooled assemblies covering the full range of circuit designs in general use today. The Assembly group offers high quality engineering support dedicated to designing new units to satisfy the growing needs of our customers.

Using the latest CAD methods our team of design and applications engineers aim to provide the Power Assembly Complete Solution (PACs).

HEATSINKS

The Power Assembly group has its own proprietary range of extruded aluminium heatsinks which have been designed to optimise the performance of Dynex semiconductors. Data with respect to air natural, forced air and liquid cooling (with flow rates) is available on request.

For further information on device clamps, heatsinks and assemblies, please contact your nearest sales representative or Customer Services.



http://www.dynexsemi.com

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Preliminary Information: The product is in design and development. The datasheet represents the product as it is understood but details may change.

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