

DIM400NSM33-F000

Single Switch IGBT Module

DS5883-1.0 March 2006 (LN24535)

FEATURES

- 10µs Short Circuit Withstand
- Soft Punch Through Silicon
- Lead Free construction
- Isolated MMC Base with AIN Substrates
- High Thermal Cycling Capability

APPLICATIONS

- High Reliability Inverters
- Motor Controllers
- Traction Drives
- Choppers

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 600V to 6500V and currents up to 3400A.

The DIM400NSM33-F000 is a single switch 3300V, soft punch through n-channel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10us short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM400NSM33-F000

Note: When ordering, please use the complete part number

KEY PARAMETERS	
N /	

3300V	
(typ) 2.8 V	
(max) 400A	
(max) 800A	
	(typ) 2.8 V (max) 400A

*(measured at the power busbars and not the auxiliary terminals)

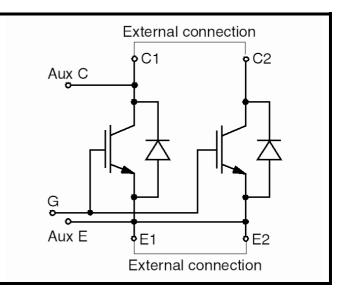
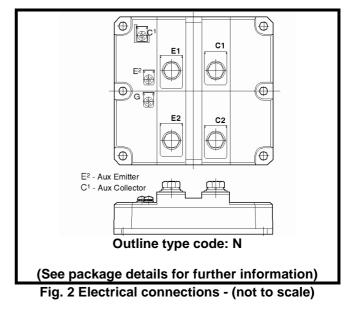


Fig. 1 Single switch circuit diagram



ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T_{case} = 25℃ unless stated otherwise

Symbo	Parameter	Test Conditions		Units
V _{CES}	Collector-emitter voltage	V _{GE} =0V	3300	V
V_{GES}	Gate-emitter voltage		±20	V
I _C	Continuous collector current	T _{case} =90 ℃	400	А
I _{C(PK)}	Peak collector current	1ms, T _{case} =115 ℃	800	A
P _{max}	Max.transistor power dissipation	T _{case} =25 ℃, T _j =150 ℃	5.2	kW
V _{isol}	Isolation voltage-per module	Commoned terminals to base plate. AC RMS,1 min,50Hz	6000	kV
Q _{PD}	Partial discharge-per module	IEC1287.V ₁ =3500V, V ₂ =2600V, 50Hz RMS	10	рС

THERMAL AND MECHANICAL RATINGS

Internal insulation material:	AIN
Baseplate material:	AISiC
Creepage distance:	29mm
Clearance:	20mm
CTI (Critical Tracking Index)	175

Symbol	Parameter	Test Conditions	Min	Тур.	Мах	Units
Rth(j-c)	Thermal resistance -transistor (per switch)	Continuous dissipation - junction to case		-	24	℃/kW
Rth(j-c)	Thermal resistance -diode (per switch)	Continuous dissipation - junction to case		-	48	°C/kW
Rth(c-h)	Thermal resistance -case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)		-	8	°C/kW
Tj	Junction temperature	Transistor	-	-	150	S
		Diode	-	-	125	C
T _{stg}	Storage temperature range	-	-40	-	125	C
	Screw torque	Mounting M6	-	-	5	Nm
		Electrical connections - M4	-	-	2	Nm
		Electrical connections - M8	-	-	10	Nm

2/8: This device is sensitive to electrostatic discharge. Users should follow ESD handling procedures.

ELECTRICAL CHARACTERISTICS

T case = 25℃ unless stated otherwise.

Symbo I	Parameter	Test Conditions	Min	Тур	Мах	Units
ICES	Collector cut-off current	V _{GE} =0V,V _{CE} =V _{CES}			2	mA
		$V_{GE} = 0V, V_{CE} = V_{CES}, T_{case} = 125 \ ^{\circ}C$			30	mA
Iges	Gate leakage current	V _{GE} = 20V,V _{CE} = 0V			1	uA
V _{GE(TH)}	Gate threshold voltage	$I_{C}=40mA, V_{GE}=V_{CE}$	5.5	6.5	7.0	V
V _{CE(sat)} †	Collector-emitter saturation voltage	V _{GE} =15V,Ic=400A		2.8		V
		V _{GE} =15V,Ic =400A,T _{VJ} =125 ℃		3.6		V
lF	Diode forward current	DC		400		А
IFM	Diode maximum forward current	t _p =1ms		800		A
V _F †	Diode forward voltage	IF =400A		2.9		V
		I⊧=400A,T _{VJ} =125 ℃		3.0		V
Cies	Input capacitance	V _{CE} =25V,V _{GE} =0V,f =1MHz		72		nF
Cres	Reverse transfer capacitance	V _{CE} =25V,V _{GE} =0V,f =1MHz		1.1		nF
Lм	Module inductance			25		nH
RINT	Internal transistor resistance			260		μΩ
SCData	Short circuit.I SC	T _j ≤125 ℃,V cc =2500V, I 1		200 0		A
		t p 10 us, I 2 V _{CE(max)} =V _{CES} -L*.di/dt IEC 60747-9		185 0		A

Note:

 † Measured at the power busbars and not the auxiliary terminals * L is the circuit inductance + L $_{M}$

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ELECTRICAL CHARACTERISTICS

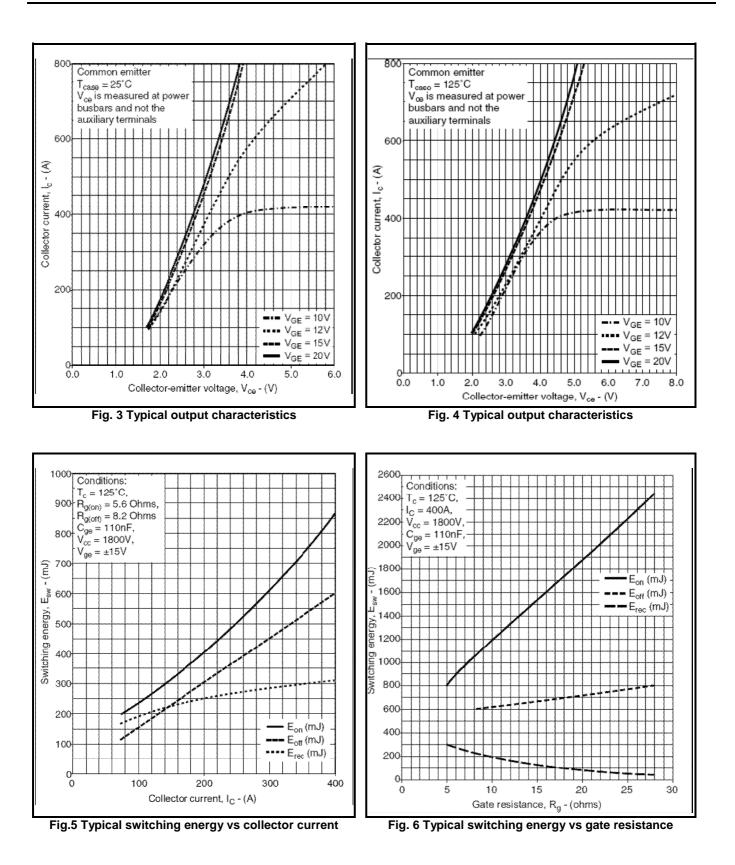
T_{case} = 25℃ unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time	I _C =400A		2.1		us
t _f	Fall time	V _{GE} =±15V		210		ns
E _{OFF}	Turn-off energy loss	V _{CE} =1800V		520		mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = R_{G(OFF)} = 8.2\Omega$		1130		ns
t _r	Rise time	C _{ge} =110nF		245		ns
Qg	Gate charge	L ~100nH		10		uC
E _{ON}	Turn-on energy loss	$I_{C} = 400A, V_{GE} = \pm 15V, \\ V_{CE} = 1800V, \\ R_{G(ON)} = 5.6\Omega, \\ C_{qe} = 110nF, L \sim 100nH$		620		mJ
Q _{rr}	Diode reverse recovery charge	I _F =400A,V _{CE} =1800V,		160		uC
I _{rr}	Diode reverse recovery current	dl _F /dt =2000A/us		330		A
E _{rec}	Diode reverse recovery energy			150		mJ

$T_{case} = 125$ °C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time	I _C =400A		2.15		us
t _f	Fall time	V _{GE} =±15V		220		ns
E _{OFF}	Turn-off energy loss	V _{CE} =1800V		600		mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = R_{G(OFF)} = 8.2\Omega$		1160		ns
t _r	Rise time	C _{ge} =110nF, L ~100nH		285		ns
E _{ON}	Turn-on energy loss	$I_{C} = 400A, V_{GE} = \pm 15V, \\ V_{CE} = 1800V, \\ R_{G(ON)} = 5.6\Omega, \\ C_{qe} = 110nF, L \sim 100nH$		870		mJ
Q _{rr}	Diode reverse recovery charge	I _F =400A,V _{CE} =1800V,		300		uC
I _{rr}	Diode reverse recovery current	dI _F /dt =2000A/us		400		A
E _{rec}	Diode reverse recovery energy]		300		mJ





Caution: This device is sensitive to electrostatic discharge. Users should follow ESD handling procedures



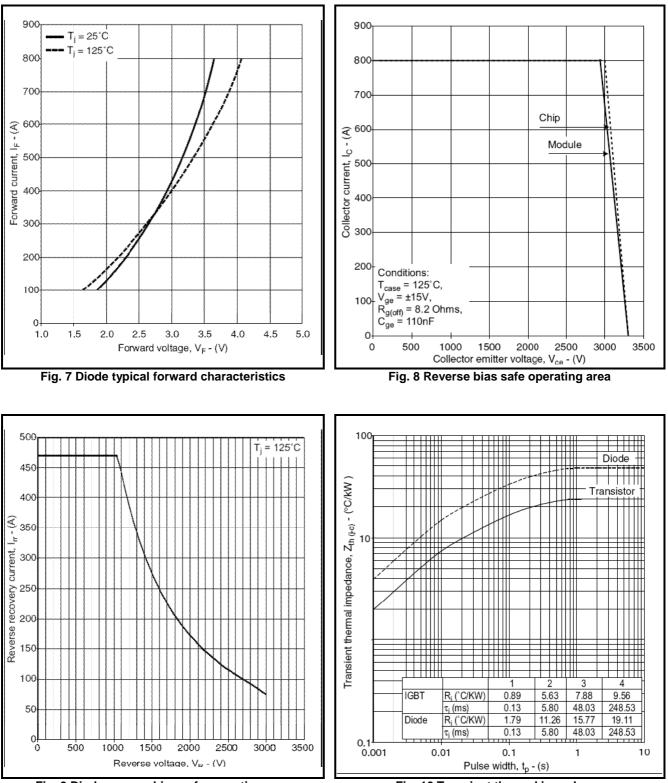


Fig. 9 Diode reverse bias safe operating area

Fig. 10 Transient thermal impedance

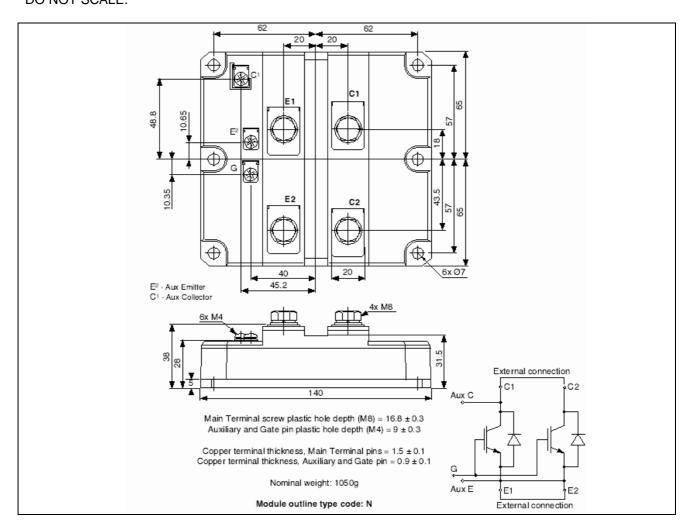
6/8: This device is sensitive to electrostatic discharge. Users should follow ESD handling procedures.





PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



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POWER ASSEMBLY CAPABILITY

The Power Assembly group was set up to provide a support service for those customers requiring more than the basic semiconductor, and has developed a flexible range of heatsink and clamping systems in line with advances in device voltages and current capability of our semiconductors.

We offer an extensive range of air and liquid cooled assemblies covering the full range of circuit designs in general use today. The Assembly group offers high quality engineering support dedicated to designing new units to satisfy the growing needs of our customers.

Using the latest CAD methods our team of design and applications engineers aim to provide the Power Assembly Complete Solution (PACs).

HEATSINKS

The Power Assembly group has its own proprietary range of extruded aluminium heatsinks which have been designed to optimise the performance of Dynex semiconductors. Data with respect to air natural, forced air and liquid cooling (with flow rates) is available on request.

For further information on device clamps, heatsinks and assemblies, please contact your nearest sales representative or Customer Services.



http://www.dynexsemi.com

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actual design work on the product has been started.

Preliminary Information: The product is in design and development. The datasheet represents the product as it is understood but details may change.

Advance Information: The product design is complete and final characterisation for volume production is well in hand.

No Annotation: The product parameters are fixed and the product is available to datasheet specification.

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