

DIM400WKS12-A000

IGBT Chopper Module - Upper Arm Control

DS5779-1.0 May 2004

FEATURES

- 10µs Short Circuit Withstand
- Non Punch Through Silicon
- Isolated Copper Baseplate

APPLICATIONS

- Inverters
- Motor Controllers

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 600V to 3300V and currents up to 3600A.

The DIM400WKS12-A000 is a 1200V, n channel enhancement mode, insulated gate bipolar transistor (IGBT) chopper module configured with the upper arm of the bridge controlled. The IGBT has a wide reverse bias safe operating area (RBSOA) plus full 10µs short circuit withstand.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM400WKS12-A000

Note: When ordering, please use the whole part number.

KEY PARAMETERS

V _{CES}		1200V
V *	(typ)	2.2V
CE(Sat)	(max)	400A
C CBR	(max)	800A

^{*(}measured at the power busbars and not the auxiliary terminals)

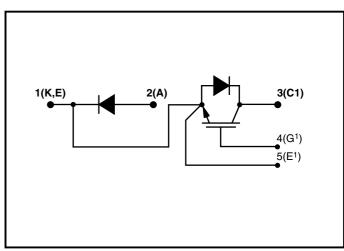


Fig. 1 Chopper circuit diagram

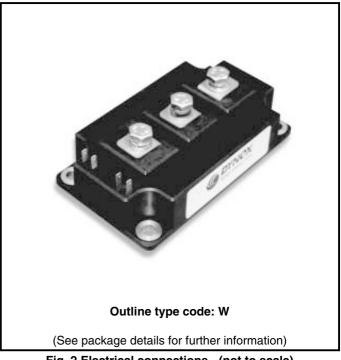


Fig. 2 Electrical connections - (not to scale)



ABSOLUTE MAXIMUM RATINGS - PER ARM

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
V _{CES}	Collector-emitter voltage	$V_{GE} = 0V$	1200	V
V _{GES}	Gate-emitter voltage	-	±20	V
I _c	Continuous collector current	$T_{case} = 80^{\circ}C$	400	Α
I _{C(PK)}	Peak collector current	1ms, T _{case} = 115°C	800	Α
P _{max}	Max. transistor power dissipation	$T_{case} = 25^{\circ}C$, $T_{j} = 150^{\circ}C$	2780	W
l²t	Diode l²t value	$V_{R} = 0, t_{p} = 10 \text{ms}, T_{vj} = 125^{\circ}\text{C}$	25	kA2s
V _{isol}	Isolation voltage - per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	2500	٧
$Q_{_{PD}}$	Partial discharge - per module	IEC1287. V ₁ = 1300V, V ₂ = 1000V, 50Hz RMS	10	PC

THERMAL AND MECHANICAL RATINGS

Internal insulation: Al₂O₃ Clearance: 13mm

Baseplate material: Cu CTI (Critical Tracking Index): 175

Creepage distance: 24mm

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
R _{th(j-c)}	Thermal resistance - transistor (per arm)	Continuous dissipation -	-	-	45	°C/kW
		junction to case				
R _{th(j-c)}	Thermal resistance - diode (per arm)	Continuous dissipation -	-	-	100	°C/kW
	(Antiparallel and freewheel diode)	junction to case				
R _{th(c-h)}	Thermal resistance - case to heatsink	Mounting torque 5Nm	-	-	15	°C/kW
	(per module)	(with mounting grease)				
T _j	Junction temperature	Transistor	-	-	150	°C
		Diode	-	-	125	°C
T _{stg}	Storage temperature range	-	-40	-	125	°C
-	Screw torque	Mounting - M6	3	-	5	Nm
		Electrical connections - M6	2.5	-	5	Nm



ELECTRICAL CHARACTERISTICS

 $T_{case} = 25$ °C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
I _{CES}	Collector cut-off current	$V_{GE} = 0V, V_{CE} = V_{CES}$	-	-	0.5	mA
	(IGBT and Diode arm)	V _{GE} = 0V, V _{CE} = V _{CES} , T _{case} = 125°C	-	-	12	mA
I _{GES}	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$	-	-	1	μА
V _{GE(TH)}	Gate threshold voltage	$I_{\rm C}$ = 20mA, $V_{\rm GE}$ = $V_{\rm CE}$	4.5	5.5	6.5	V
V _{CE(sat)} †	Collector-emitter saturation voltage	V _{GE} = 15V, I _C = 400A	-	2.2	2.7	V
		V _{GE} = 15V, I _C = 400A, , T _{case} = 125°C	-	2.6	3.1	V
I _F	Diode forward current	DC	-	-	400	А
I _{FM}	Diode maximum forward current	t _p = 1ms	-	-	800	А
V _F †	Diode forward voltage	I _F = 400A	-	2.2	2.5	V
	(IGBT and Diode arm)	I _F = 400A, T _{case} = 125°C	-	2.3	2.6	V
C _{ies}	Input capacitance	V _{CE} = 25V, V _{GE} = 0V, f = 1MHz	-	66	-	nF
L _M	Module inductance - per arm	-	-	20	-	nH
R _{INT}	Internal transistor resistance - per arm	-	-	0.23	-	mΩ
SC _{Data}	Short circuit. I _{sc}	$T_j = 125^{\circ}C, V_{cc} = 900V,$ I_1	-	2750	-	Α
		$t_p \le 10\mu s$, $V_{CE(max)} = V_{CES} - L^*$. di/dt	-	2250	-	А
		IEC 60747-9				

Note:

 $^{^{\}scriptscriptstyle \dagger}$ Measured at the power busbars and not the auxiliary terminals

 L^* is the circuit inductance + $L_{\rm M}$



ELECTRICAL CHARACTERISTICS

T_{case} = 25°C unless stated otherwise

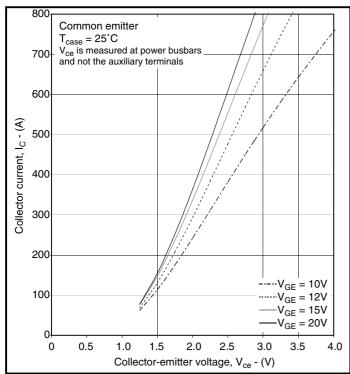
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
t _{d(off)}	Turn-off delay time	I _C = 400A	-	710	-	ns
t _f	Fall time	$V_{GE} = \pm 15V$	-	70	-	ns
E _{OFF}	Turn-off energy loss	V _{CE} = 600V	-	45	-	mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = R_{G(OFF)} = 3.3\Omega$	-	190	-	ns
t _r	Rise time	L ~ 100nH	-	100	-	ns
E _{on}	Turn-on energy loss		-	45	-	mJ
Q_g	Gate charge		-	4	-	μС
Q _{rr}	Diode reverse recovery charge	I _F = 400A, V _R = 600V,	-	55	-	μС
I _{rr}	Diode reverse current	dl _F /dt = 4700A/μs	-	300	-	Α
E _{REC}	Diode reverse recovery energy		-	17	-	mJ

T_{case} = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
t _{d(off)}	Turn-off delay time	I _C = 400A	-	890	-	ns
t _r	Fall time	$V_{GE} = \pm 15V$	-	100	-	ns
E _{OFF}	Turn-off energy loss	V _{CE} = 600V	-	60	-	mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = R_{G(OFF)} = 3.3\Omega$	-	440	-	ns
t _r	Rise time	L ~ 100nH	-	125	-	ns
E _{on}	Turn-on energy loss		-	60	-	mJ
Q _{rr}	Diode reverse recovery charge	I _F = 400A, V _R = 600V,	-	85	-	μС
l _{rr}	Diode reverse current	$dI_F/dt = 4000A/\mu s$	-	320	-	А
E _{REC}	Diode reverse recovery energy		-	32	-	mJ



TYPICAL CHARACTERISTICS



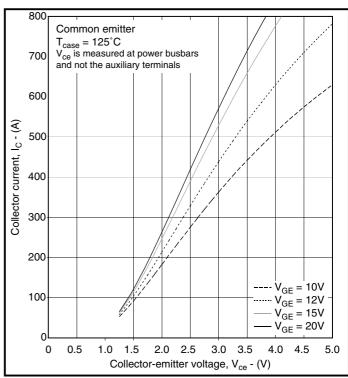


Fig. 3 Typical output characteristics

Fig. 4 Typical output characteristics

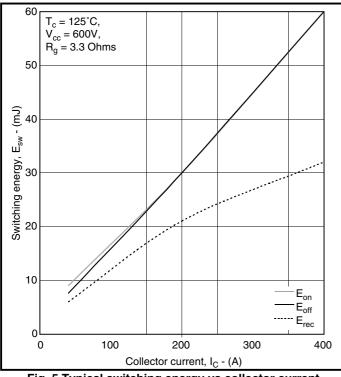


Fig. 5 Typical switching energy vs collector current

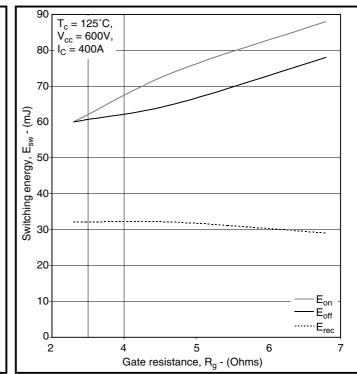
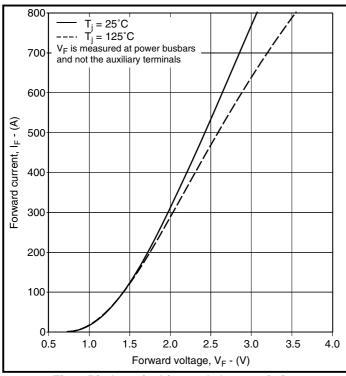


Fig. 6 Typical switching energy vs gate resistance





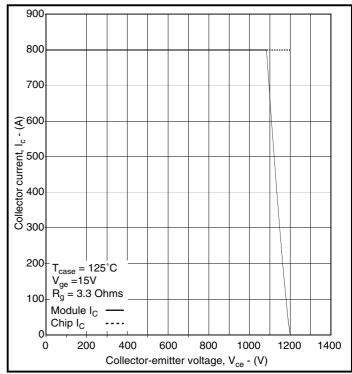
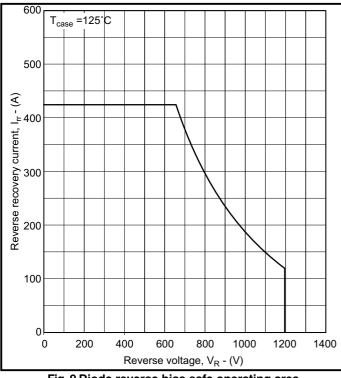
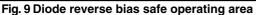


Fig. 7 Diode typical forward characteristics

Fig. 8 Reverse bias safe operating area





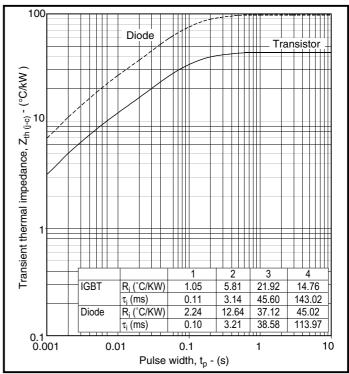


Fig. 10 Transient thermal impedance

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PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.

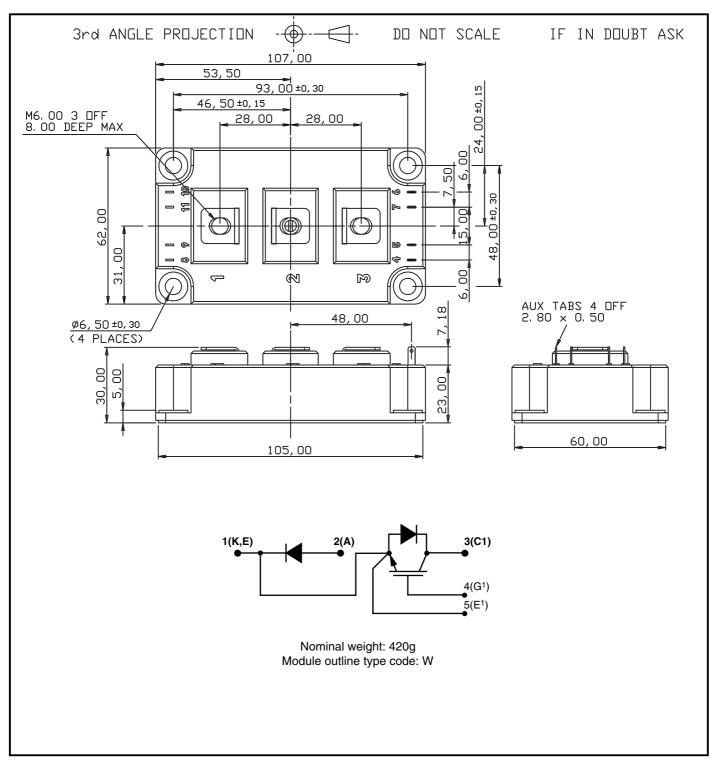


Fig. 11 Package details



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We offer an extensive range of air and liquid cooled assemblies covering the full range of circuit designs in general use today. The Assembly group offers high quality engineering support dedicated to designing new units to satisfy the growing needs of our customers.

Using the latest CAD methods our team of design and applications engineers aim to provide the Power Assembly Complete Solution (PACs).

HEATSINKS

The Power Assembly group has its own proprietary range of extruded aluminium heatsinks which have been designed to optimise the performance of Dynex semiconductors. Data with respect to air natural, forced air and liquid cooling (with flow rates) is available on request.

For further information on device clamps, heatsinks and assemblies, please contact your nearest sales representative or Customer Services.



http://www.dynexsemi.com

e-mail: power_solutions@dynexsemi.com

HEADQUARTERS OPERATIONS **DYNEX SEMICONDUCTOR LTD**Doddington Road, Lincoln.

Lincolnshire. LN6 3LF. United Kingdom.

Tel: +44-(0)1522-500500 Fax: +44-(0)1522-500550 CUSTOMER SERVICE

Tel: +44 (0)1522 502753 / 502901. Fax: +44 (0)1522 500020

SALES OFFICES

 $\textbf{Benelux, Italy \& Switzerland:} \ \, \textbf{Tel: +33 (0)1 60 69 32 36.} \ \, \textbf{Fax: +33 (0)1 60 69 31 97}.$

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Germany, Northern Europe, Spain & Rest Of World: Tel: +44 (0)1522 502753 / 502901.

Fax: +44 (0)1522 500020

 $\textbf{North America:} \ \, \text{Tel:} \ \, (440) \ \, 259\text{-}2060. \ \, \text{Fax:} \ \, (440) \ \, 259\text{-}2059. \ \, \text{Tel:} \ \, (949) \ \, 733\text{-}3005. \ \, \text{Fax:} \ \, (949) \ \, 733\text{-}2986.$

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