DIM800DDS12-A000



Dual Switch IGBT Module

DS5540-2.2 June 2005 (LN24051)

FEATURES

- 10µs Short Circuit Withstand
- Non Punch Through Silicon
- Isolated Copper Baseplate
- Lead Free construction

APPLICATIONS

- High Power Inverters
- Motor Controllers

The Powerline range of high power modules includes half bridge, chopper, dual, single and bidirectional switch configurations covering voltages from 600V to 3300V and currents up to 2400A.

The DIM800DDS12-A000 is a dual switch 1200V, n channel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus full 10µs short circuit withstand.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM800DDS12-A000

Note: When ordering, please use the whole part number.

KEY PARAMETERS

V_{CES}		1200V
V _{CE (sat)} *	(typ)	2.2V
I _c	(max)	A008
I _{C(PK)}	(max)	1600A

⁽measured at the power busbars and not the auxiliary terminals)

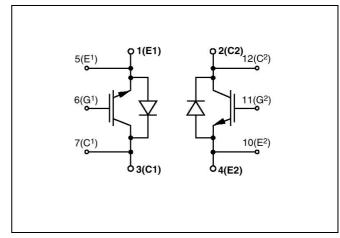


Fig. 1 Dual switch circuit diagram

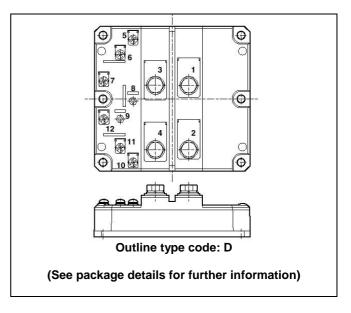


Fig. 2 Electrical connections (not to scale)

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ABSOLUTE MAXIMUM RATINGS - PER ARM

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

Tcase = 25° C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
V _{CES}	Collector-emitter voltage	$V_{GE} = 0V$	1200	V
V _{GES}	Gate-emitter voltage		±20	V
Ic	Continuous collector current	T _{case} = 85° C	800	Α
I _{C(PK)}	Peak collector current	1ms, T _{case} =115° C	1600	Α
P _{max}	Max. transistor power dissipation	T _{case} = 25° C, T _j = 150° C	6940	W
l ² t	Diode I ² t value	$V_R = 0$, $t_P = 10$ ms, $T_{vj} = 125$ ° C	100	kA ² S
V _{isol}	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	2500	V



THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
R _{th(j-c)}	Thermal resistance – transistor (per arm)	Continuous dissipation – junction to case	-	-	18	° C/kW
R _{th(j-c)}	Thermal resistance – diode (per arm)	Continuous dissipation – junction to case	-	-	40	° C/kW
R _{th(c-h)}	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	-	8	° C/kW
Tj	Junction temperature	Transistor	-	-	150	°C
		Diode	-	-	125	°C
T _{stg}	Storage temperature range	-	-40	-	125	°C
-	Screw torque	Mounting – M6	-	-	5	Nm
	·	Electrical connections – M4	-	-	2	Nm
		Electrical connections – M8	-	-	10	Nm



ELECTRICAL CHARACTERISTICS

T_{case} = 25° C unless stated otherwise.

Symbol	Parameter	Test Conditions		Min.	Тур.	Max.	Units
I _{ces}	Collector cut-off current	V _{GE} = OV, V _{CE} = V _{CES}		-	-	1	mA
		V _{GE} = OV, V _{CE} = V _{CES} , T _{case} = 125° (С	-	-	25	mA
I _{ces}	Gate leakage current	V _{GE} = ±20V, V _{CE} = 0V		-	-	4	μA
V _{GE(TH)}	Gate threshold voltage	$I_C = 40$ mA, $V_{GE} = V_{CE}$		4.5	5.5	6.5	V
$V_{CE(sat)^{\dagger}}$	Collector-emitter saturation voltage	V _{GE} = 15V, I _C = 800A		-	2.2	2.8	V
		V _{GE} = 15V, I _C = 800A, T _{case} = 125° C	5	-	2.6	3.2	V
I _F	Diode forward current	DC		-	-	800	Α
I _{FM}	Diode maximum forward current	t _p = 1ms		-	-	1600	Α
V_{F^\dagger}	Diode forward voltage	I _F = 800A		-	2.1	2.4	V
		I _F = 800A, T _{case} = 125° C		-	2.1	2.4	V
Cies	Input capacitance	V _{CE} = 25V, V _{GE} = 0V, f = 1MHz		-	90	-	nF
L _M	Module inductance	-		-	20	-	nH
R _{INT}	Internal transistor resistance	-		-	0.27	-	mΩ
SC _{Data}	Short circuit. I _{sc}	$T_j = 125^{\circ} C, V_{cc} = 900V,$	I ₁	-	5500	-	Α
		$t_p \le 10\mu s,$ $V_{CE(max)} = V_{CES} - L^* \times di/dt$ $IEC 60747-9$	l ₂	-	4500	-	A

Note:

 $^{^{\}scriptscriptstyle \dag}$ Measured at the power busbars and not the auxiliary terminals * L is the circuit inductance + L_{M}



ELECTRICAL CHARACTERISTICS

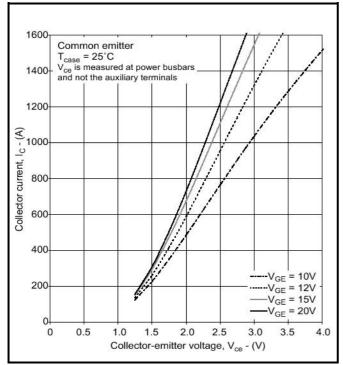
 T_{case} = 25° C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
$t_{d(off)}$	Turn-off delay time	I _C = 800A	-	1250	-	ns
t _f	Fall time	V _{GE} = ±15V	-	170	-	ns
E _{OFF}	Turn-off energy loss	V _{CE} =600V	-	130	-	mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = R_{G(OFF)} = 2.7\Omega$	-	250	-	ns
t _r	Rise time	L ~ 100nH	-	250	-	ns
Eo	Turn-on energy loss		-	80	-	mJ
Q_g	Gate charge		-	9	-	μC
Q _{rr}	Diode reverse recovery charge	$I_F = 800A$, $V_R = 600V$,	-	80	-	μC
Irr	Diode reverse current	dl _F /dt = 4200A/µs	-	380	-	А
E _{REC}	Diode reverse recovery energy		-	30	-	mJ

T_{case} = 125° C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
t _{d(off)}	Turn-off delay time	I _C = 800A	-	1500	-	ns
t _f	Fall time	V _{GE} = ±15V	-	200	-	ns
E _{OFF}	Turn-off energy loss	V _{CE} = 600V	-	160	-	mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = R_{G(OFF)} = 2.7\Omega$	-	400	-	ns
t _r	Rise time	L ~ 100nH	-	220	-	ns
Eon	Turn-on energy loss		-	120	-	mJ
Q _{rr}	Diode reverse recovery charge	$I_F = 800A$, $V_R = 600V$,	-	160	-	μC
I _{rr}	Diode reverse current	dl _F /dt = 4000A/µs	-	450	-	Α
E _{REC}	Diode reverse recovery energy		-	60	-	mJ





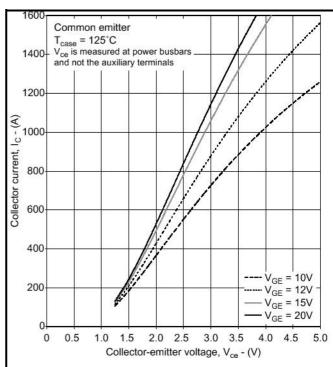
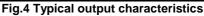
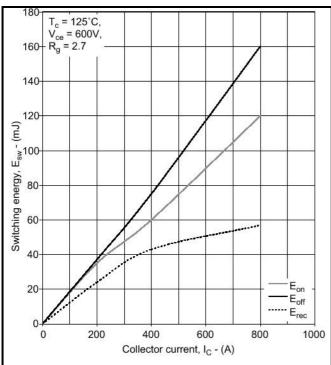
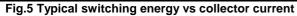


Fig.3 Typical output characteristics







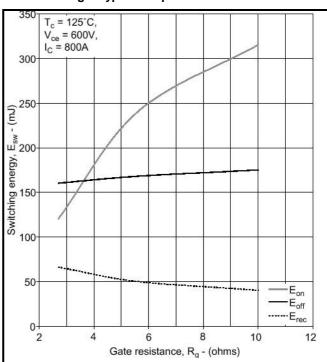
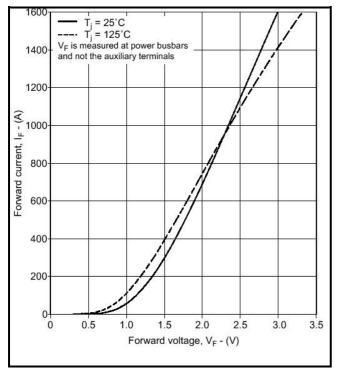


Fig.6 Typical switching energy vs gate resistance





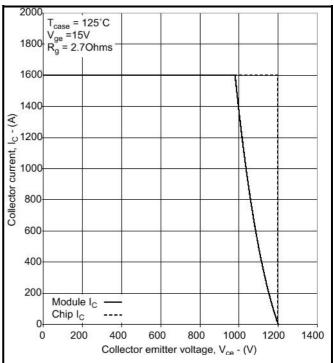
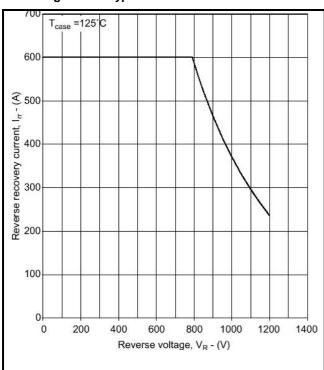
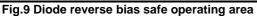


Fig.7 Diode typical forward characteristics







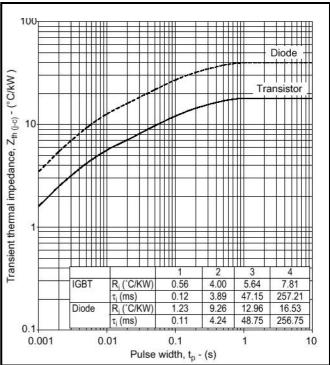


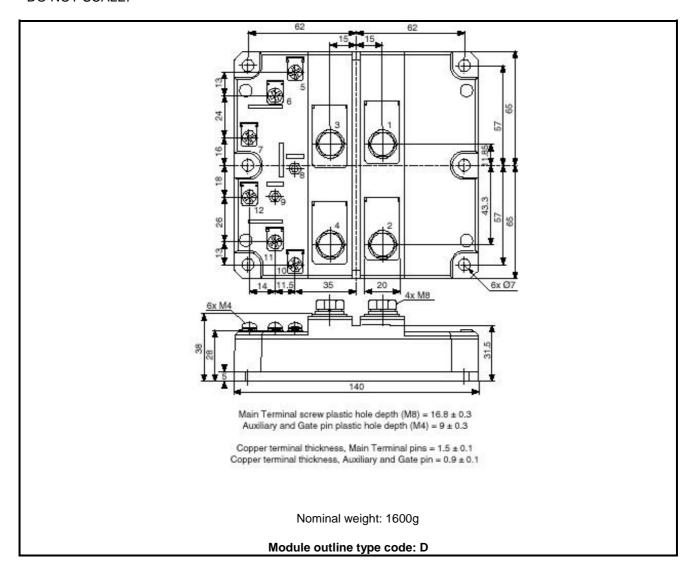
Fig.10 Transient thermal impedance



PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise.

DO NOT SCALE.





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