PRELIMINARY SPEC.

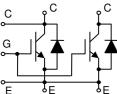
BN1600EB17D

Silicon N-channel IGBT

FEATURES

- * High speed, low loss IGBT module.
- * Low driving power due to low input
- capacitance MOS gate.
- * Low noise due to ultra soft fast recovery diode.
- * High reliability, high durability module.
- * High thermal fatigue durability. (delta Tc=70°C, N>30,000cycles)
- * Isolated heat sink (terminal to base).

CIRCUIT DIAGRAM



ABSOLUTE MAXIMUM RATINGS (Tc=25°C)

Unit MBN1600EB17D Symbol Item Collector Emitter Voltage 1,700 VCES V Gate Emitter Voltage V_{GES} V ±20 DC lc 1,600 Collector Current A 1ms I_{Cp} 3.200 DC 1,600 I_{F} Forward Current А 1ms 3,200 I_{FM} °C Junction Temperature -40 ~ +125 Ti °C -40 ~ +125 Storage Temperature T_{stg} Isolation Voltage VISO V_{RMS} 4,000(AC 1 minute) Terminals (M4/M8) 2/10 (1)Screw Torque N·m Mounting (M6) 6 (2)

Notes: (1) Recommended Value 1.8±0.2/9±1N·m

(2) Recommended Value 5.5±0.5N·m

ELECTRICAL	CHARACTERISTICS

Iten	n	Symbol	Unit	Min.	Тур.	Max.	Test Conditions
Collector Emitter Cut-Off Current		I _{CES}	mA	-	-	10	V _{CE} =1,700V, V _{GE} =0V, Tj=25°C
				-	10	35	V _{CE} =1,700V, V _{GE} =0V, Tj=125°C
Gate Emitter Leakage Current		I _{GES}	nA	-500	-	+500	V _{GE} =±20V, V _{CE} =0V, Tj=25°C
Collector Emitter Saturation Voltage		V _{CE(sat)}	V	-	2.7	3.3	I _C =1,600A, V _{GE} =15V, Tj=125°C
Gate Emitter Threshold Voltage		V _{GE(TO)}	V	5.5	7.0	8.5	V _{CE} =10V, I _C =160mA, Tj=25°C
Input Capacitance		Cies	nF	-	140	-	V _{CE} =10V, V _{GE} =0V, f=100kHz, Tj=25°C
Internal Gate Resistance		Rge	Ω	-	0.8	-	V _{CE} =10V, V _{GE} =0V, f=100kHz, Tj=25°C
Switching Times	Rise Time	tr	μs	-	0.7	1.4	V _{CC} =900V, Ic=1,600A
	Turn On Time	t _{on}		-	1.2	2.4	$L=65nH,C_{GE}=150nF(TBD)$ (3)
	Fall Time	t _f		-	0.2	0.4	$R_{G}=1.5\Omega(TBD)$ (3)
	Turn Off Time	t _{off}		-	1.9	3.8	V _{GE} =±15V, Tj=125°C
Peak Forward Voltage Drop		V _{FM}	V	-	1.9	2.5	Ic=1,600A, V _{GE} =0V, Tj=125°C
Reverse Recovery Time		t _{rr}	μs	-	0.7	1.1	V _{CC} =900V, Ic=1,600A
Turn On Loss		E _{on(10%)}	J/P	-	0.5	0.8	$L=65nH,C_{GE}=150nF(TBD)$ (3)
Turn Off Loss		E _{off(10%)}	J/P	-	0.5	0.8	$R_{G}=1.5\Omega(TBD)$ (3)
Reverse Recovery Loss		E _{rr(10%)}	J/P	-	0.5	0.8	V _{GE} =±15V, Tj=125°C
Stray inductance module		LSCE	nH	-	18	-	
Thermal Impedance	IGBT	Rth(j-c)	°C/W	-	-	0.015	Junction to case
	FWD	Rth(j-c)		-	-	0.023	
Contact Thermal Impedance		Rth(c-f)	°C/W	-	0.008	-	Case to fin

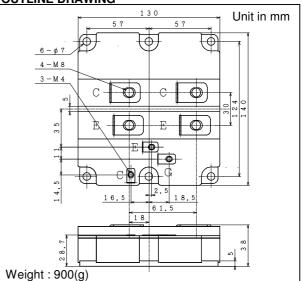
Notes:(3) R_G and C_{GE} value is the test condition's value for evaluation of the switching times, not recommended value. Please, determine the suitable R_G and C_{GE} value after the measurement of switching waveforms (overshoot voltage, etc.) with appliance mounted.

* Please contact our representatives at order.

* For improvement, specifications are subject to change without notice.

* For actual application, please confirm this spec sheet is the newest revision.

OUTLINE DRAWING



HITACHI POWER SEMICONDUCTORS

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