

MBN1800E17DD

TARGET SPECIFICATION

Silicon N-channel IGBT

1. FEATURES

- * High speed, low loss IGBT module due to LiPT Trench Technology
- * Low noise due to ultra soft fast recovery diode. (U-SFD)
- * High reverse recovery capability (HiRC)
- * High thermal fatigue durability. ($\Delta T_c=70^\circ\text{C}$, $N>30,000$ cycles)

2. ABSOLUTE MAXIMUM RATINGS ($T_c=25^\circ\text{C}$)

Item	Symbol	Unit	MBN1800E17DD	
Collector Emitter Voltage	V_{CES}	V	1,700	
Gate Emitter Voltage	V_{GES}	V	± 20	
Collector Current	DC	I_C	1,800	
	1ms	I_{Cp}	3,600	
Forward Current	DC	I_F	1,800	
	1ms	I_{FM}	3,600	
Junction Temperature	T_j	$^\circ\text{C}$	-40 ~ +125	
Storage Temperature	T_{stg}	$^\circ\text{C}$	-40 ~ +125	
Isolation Voltage	V_{ISO}	V_{RMS}	4,000 (AC 1 minute)	
Screw Torque	Terminals	(M4)	-	2 ⁽¹⁾
		(M8)	-	15 ⁽¹⁾
	Mounting	(M6)	-	6 ⁽²⁾

Notes: (1) Recommended Value $1.8\pm 0.2 / 15^{+0}_{-3}$ N·m(2) Recommended Value 5.5 ± 0.5 N·m

3. ELECTRIC CHARACTERISTICS

Item	Symbol	Unit	Min.	Typ.	Max.	Test Conditions
Collector Emitter Cut-Off Current	I_{CES}	mA	-	-	12	$V_{CE}=1,700\text{V}$, $V_{GE}=0\text{V}$, $T_j=25^\circ\text{C}$
			-	15	50	$V_{CE}=1,700\text{V}$, $V_{GE}=0\text{V}$, $T_j=125^\circ\text{C}$
Gate Emitter Leakage Current	I_{GES}	nA	-500	-	+500	$V_{GE}=\pm 20\text{V}$, $V_{CE}=0\text{V}$, $T_j=25^\circ\text{C}$
Collector Emitter Saturation Voltage	$V_{CE(sat)}$	V	-	2.2	-	$I_C=1,800\text{A}$, $V_{GE}=15\text{V}$, $T_j=25^\circ\text{C}$
			-	2.7	3.3	$I_C=1,800\text{A}$, $V_{GE}=15\text{V}$, $T_j=125^\circ\text{C}$
Gate Emitter Threshold Voltage	$V_{GE(TO)}$	V	5.0	6.5	8.0	$V_{CE}=10\text{V}$, $I_C=180\text{mA}$, $T_j=25^\circ\text{C}$
Input Capacitance	C_{ies}	nF	-	150	-	$V_{CE}=10\text{V}$, $V_{GE}=0\text{V}$, $f=100\text{kHz}$, $T_j=25^\circ\text{C}$
Gate Charge	Q_G	μC	-	12	-	$V_{GE}=\pm 15\text{V}$, $V_{CC}=900\text{V}$, $I_C=1,800\text{A}$
Internal Gate Resistance (Tentative)	$R_{ge(int)}$	Ω	-	0.9	-	$V_{CE}=10\text{V}$, $V_{GE}=0\text{V}$, $f=100\text{kHz}$, $T_j=25^\circ\text{C}$
Switching Times	Rise Time	t_r	-	0.8	1.6	$V_{CC}=900\text{V}$, $I_C=1,800\text{A}$
	Turn On Time	t_{on}	-	1.3	2.6	$L=55\text{nH}$, $C_{GE}=180\text{nF}$ ⁽³⁾
	Fall Time	t_f	-	0.2	0.4	$R_G=1.5\Omega$ ⁽³⁾
	Turn Off Time	t_{off}	-	1.5	3.0	$V_{GE}=\pm 15\text{V}$, $T_j=125^\circ\text{C}$
Peak Forward Voltage Drop	V_{FM}	V	-	1.6	-	$I_C=1,800\text{A}$, $V_{GE}=0\text{V}$, $T_j=25^\circ\text{C}$
			-	1.7	2.3	$I_C=1,800\text{A}$, $V_{GE}=0\text{V}$, $T_j=125^\circ\text{C}$
Reverse Recovery Time	t_{rr}	μs	-	0.7	1.4	
Turn On Loss	$E_{on(10\%)}$	J/P	-	0.65	1.0	
	$E_{on(Full)}$	J/P	-	0.7	(1.05)	$V_{CC}=900\text{V}$, $I_C=1,800\text{A}$ ⁽³⁾
Turn Off Loss	$E_{off(10\%)}$	J/P	-	0.58	0.9	$L=55\text{nH}$, $C_{GE}=180\text{nF}$ ⁽³⁾
	$E_{off(Full)}$	J/P	-	0.65	(1.05)	$R_G=1.5\Omega$ ⁽³⁾
Reverse Recovery Loss	$E_{rr(10\%)}$	J/P	-	0.68	1.1	$V_{GE}=\pm 15\text{V}$, $T_j=125^\circ\text{C}$
	$E_{rr(Full)}$	J/P	-	0.8	(1.2)	
Reverse Recovery Peak Current	I_{RRM}	A	-	1800	-	
RBSOA	I_C	A	3600	-	-	$V_{CC}=1000\text{V}$, $L=55\text{nH}$, $C_{GE}=180\text{nF}$ ⁽³⁾
Recovery SOA	I_F	A	3600	-	-	$R_G=1.5\Omega$ ⁽³⁾ , $V_{GE}=\pm 15\text{V}$, $T_j=125^\circ\text{C}$
I^2t value	I^2t	kA^2s	-	1000	-	$T_{j,start}=125^\circ\text{C}$, 10ms, $V_R=0\text{V}$
Partial Discharge Extinction Voltage	V_{PDoff}	V_{RMS}	1.3	-	-	$Q=10\text{pC}$, 50Hz,

Notes: (3) R_G and C_{GE} value is the test condition's value for evaluation of the switching times, not recommended value.Please, determine the suitable R_G and C_{GE} value after the measurement of switching waveforms (overshoot voltage, etc.) with appliance mounted.

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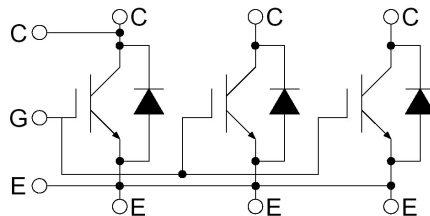
4. THERMAL CHARACTERISTICS

Item		Symbol	Unit	Min.	Typ.	Max.	Test Conditions
Thermal Resistance	IGBT	$R_{th(j-c)}$	$^{\circ}\text{C}/\text{W}$	-	-	0.013	Junction to case
	FWD	$R_{th(j-c)}$		-	-	0.015	
Contact	Thermal Impedance	$R_{th(c-f)}$	$^{\circ}\text{C}/\text{W}$	-	0.006	-	Case to fin. Thermal grease applied. Thickness 100 μm , Thermal conductivity of grease: 1W/mK

5. MODULE MECHNICAL CHARACTERISTICS

Item		Unit	Characteristics	Conditions
Weight		g	1300	
Cree page Distance	Between terminal	mm	22	
	Terminal-Base	mm	19.5	
Clearance Distance	Between terminal	mm	35	
	Terminal-Base	mm	35	
Stray inductance in module	LS(CM-EM)	nH	12	Collector-main to Emitter-main
	LS(ES-EM)		3.8	Emitter-sense to Emitter-main
	LS(CM-CS)		6.4	Collector-main to Collector sense
Terminal Resistance	R_{Terminal}	m Ω	0.09	Collector-main to Emitter-main
Comparative Tracking Index (CTI)			600	
Module base plate Material			Al-SiC	
Baseplate Thickness		mm	5	
Insulation Material			AlN	
Terminal Surface treatment			Ni plating	
Case Material			Poly-Phenilene Sulfide	
Fire and Smoke Category			I2 / F3	NFF 16-102

6. CIRCUIT DIAGRAM

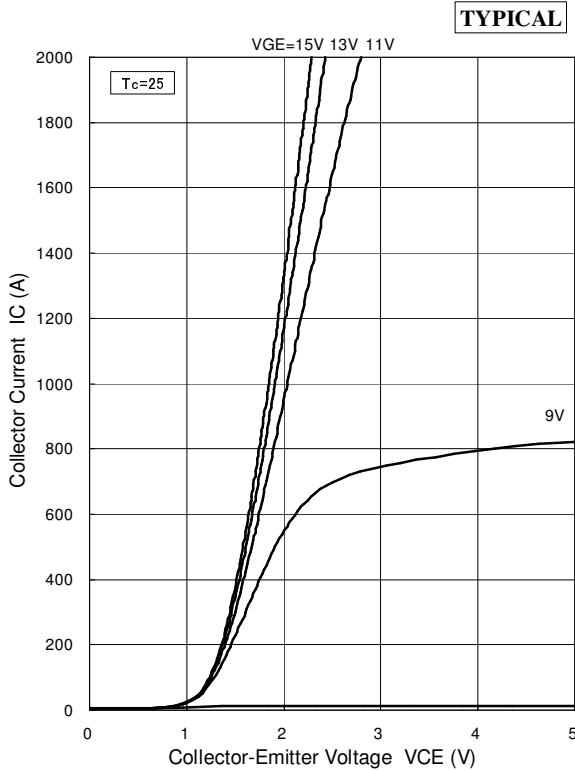


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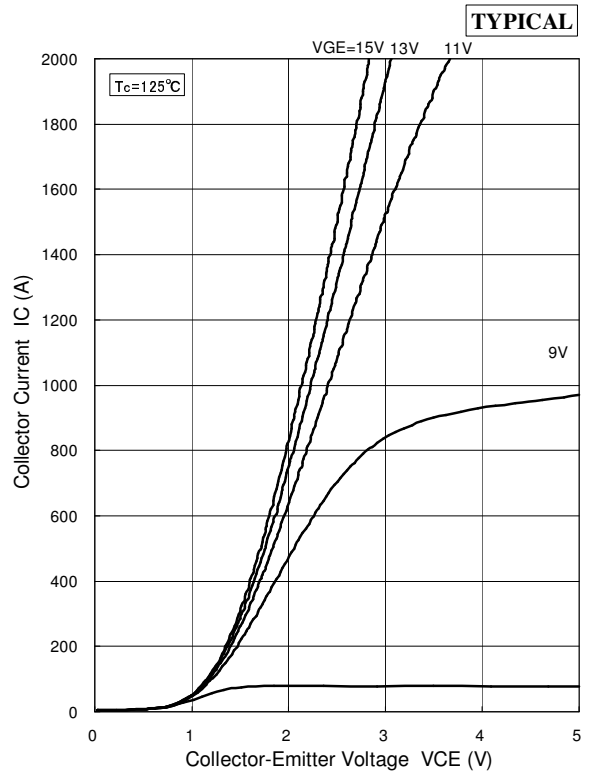
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7. CHARACTERISTICS CURVE

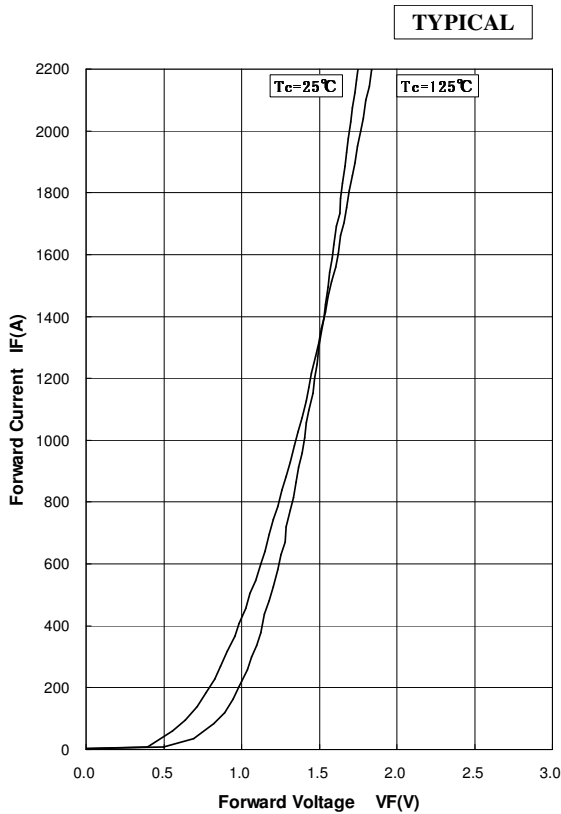
7.1 STATIC CHARACTERISTICS



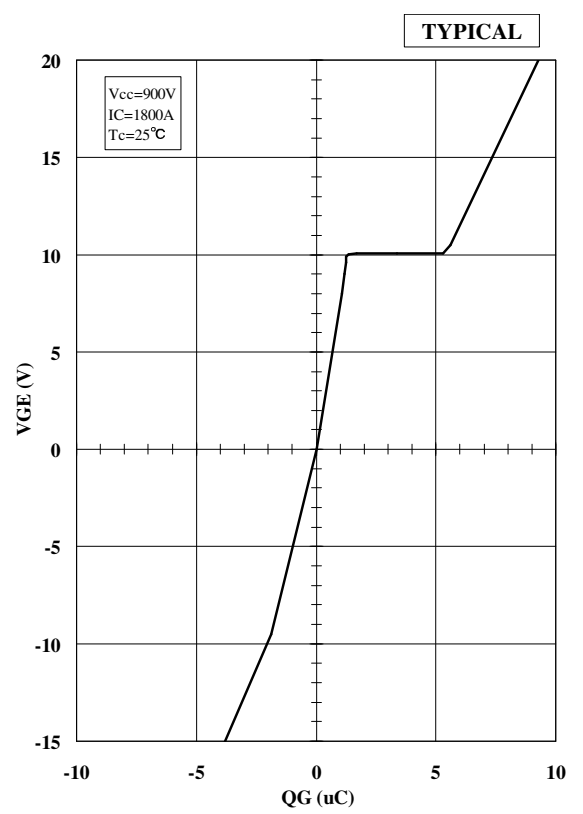
Collector Current vs. Collector to Emitter Voltage



Collector Current vs. Collector to Emitter Voltage



Forward Voltage of free-wheeling diode



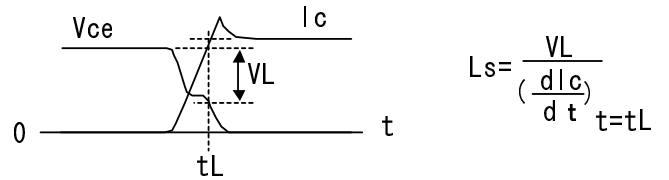
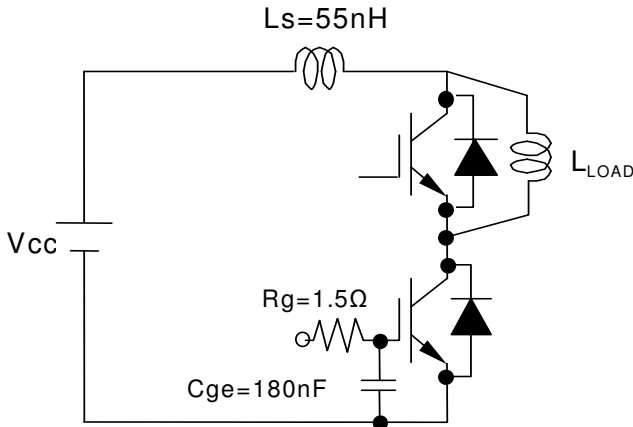
QG-VGE curve

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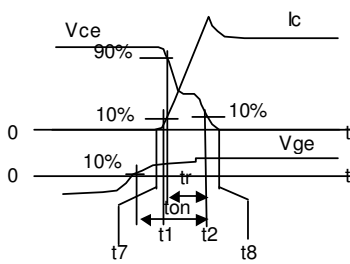
7.2 DYNAMIC CHARACTERISTICS

7.2.1 CIRCUIT



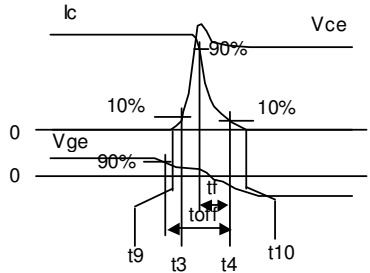
Definitions of L_s

7.2.2 WAVEFORM DEFINITION



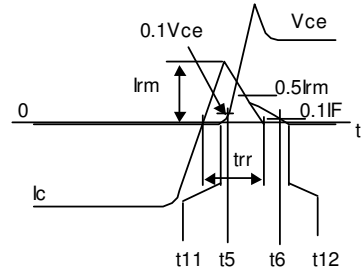
$$E_{on(10\%)} = \int_{t1}^{t2} I_c \cdot V_{ce} dt$$

$$E_{on(Full)} = \int_{t7}^{t8} I_c \cdot V_{ce} dt$$



$$E_{off(10\%)} = \int_{t3}^{t4} I_c \cdot V_{ce} dt$$

$$E_{off(Full)} = \int_{t9}^{t10} I_c \cdot V_{ce} dt$$



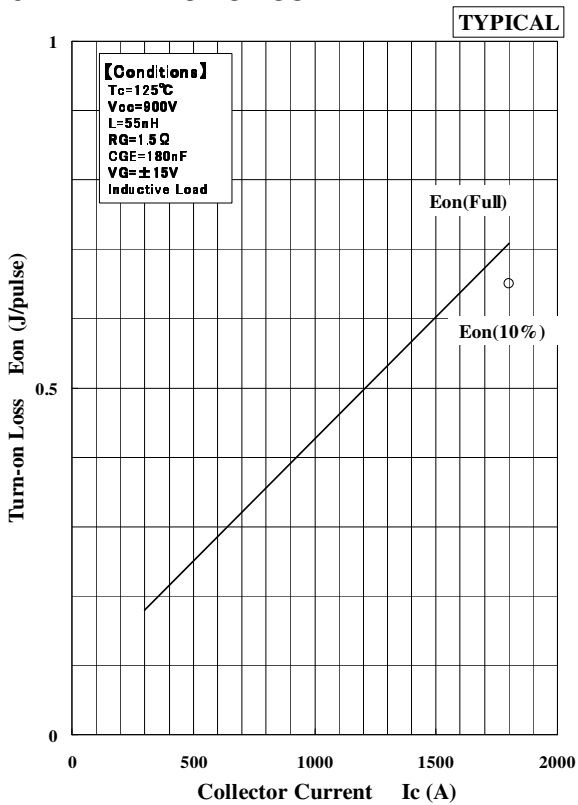
$$Err(10\%) = \int_{t5}^{t6} I_F \cdot V_{ce} dt$$

$$Err(Full) = \int_{t11}^{t12} I_F \cdot V_{ce} dt$$

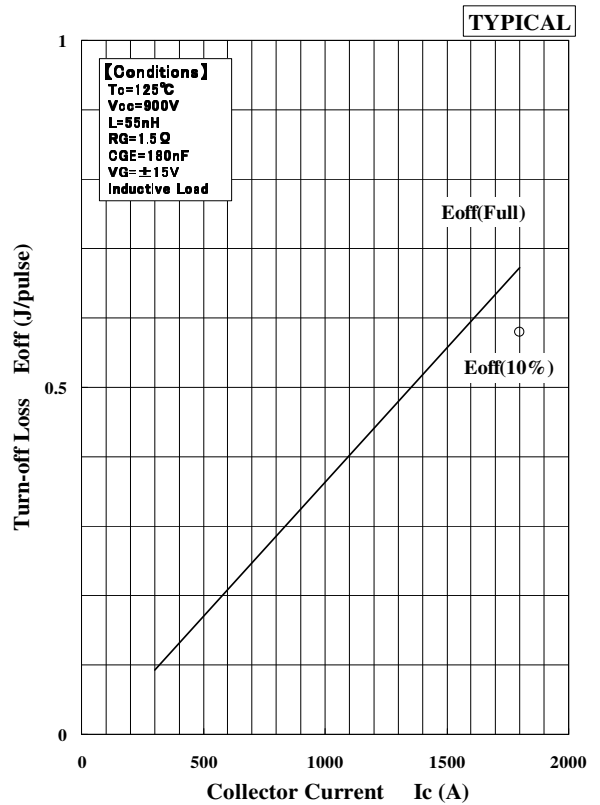
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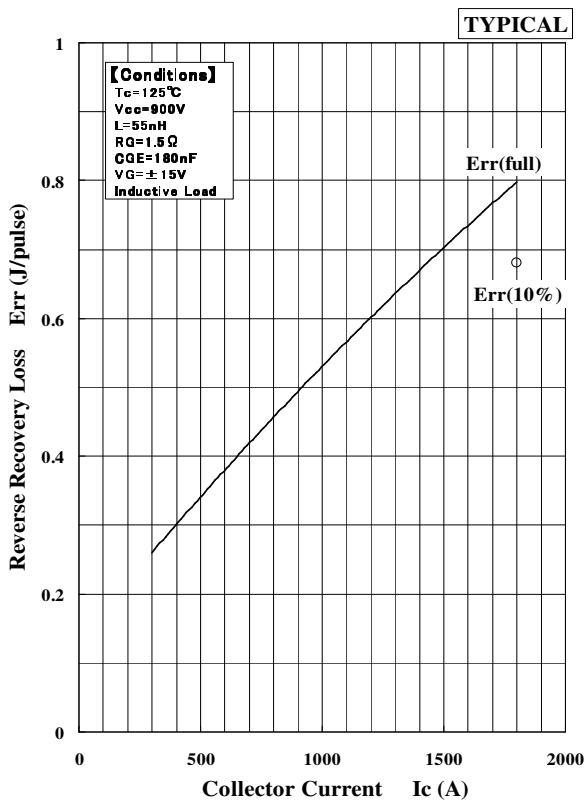
7.2.3 DEPENDENCE OF CURRENT



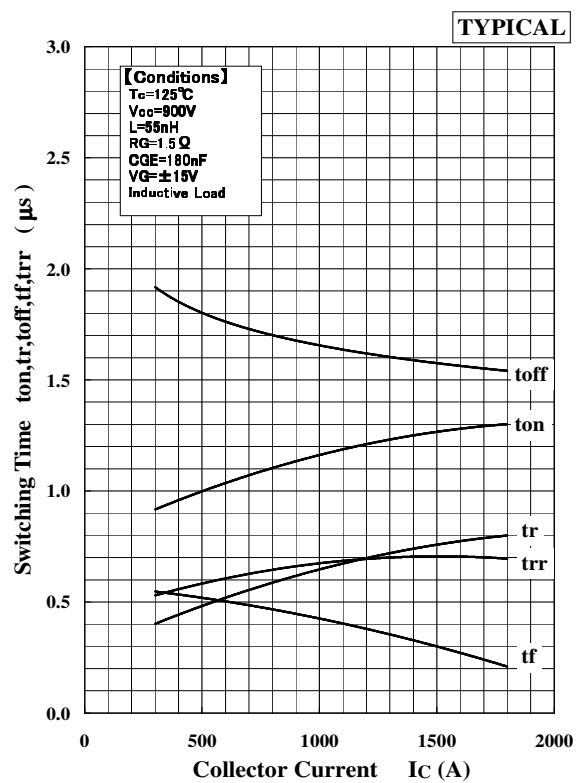
Turn-on Loss vs. Collector Current



Turn-off Loss vs. Collector Current



Recovery Loss vs. Collector Current

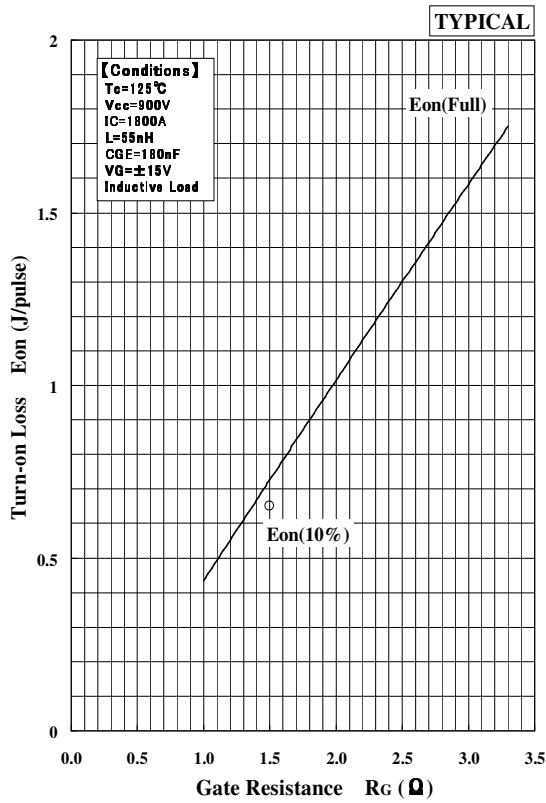


Switching Time vs. Collector Current

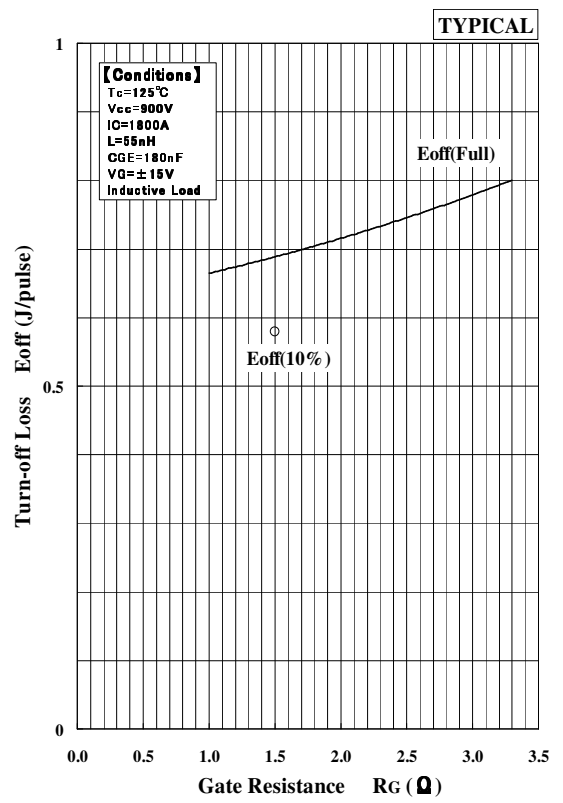
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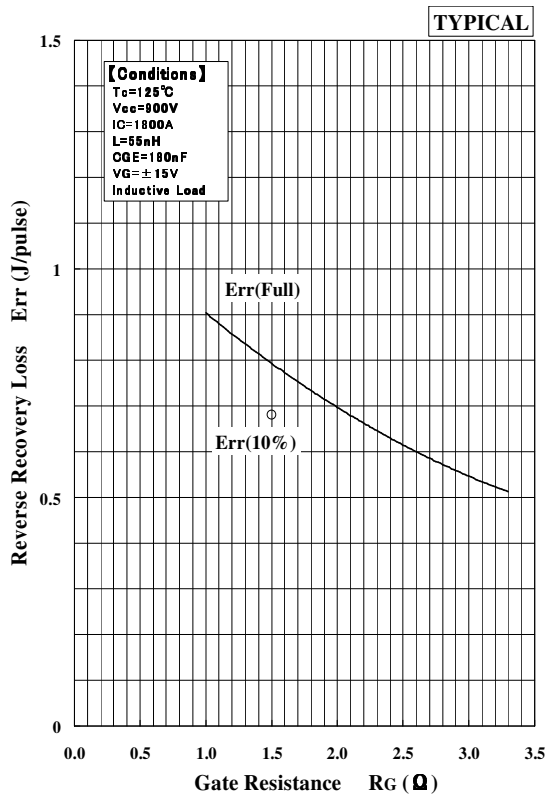
7.2.4 DEPENDENCE OF RG



Turn-on Loss vs. Gate Resistance



Turn-off Loss vs. Gate Resistance

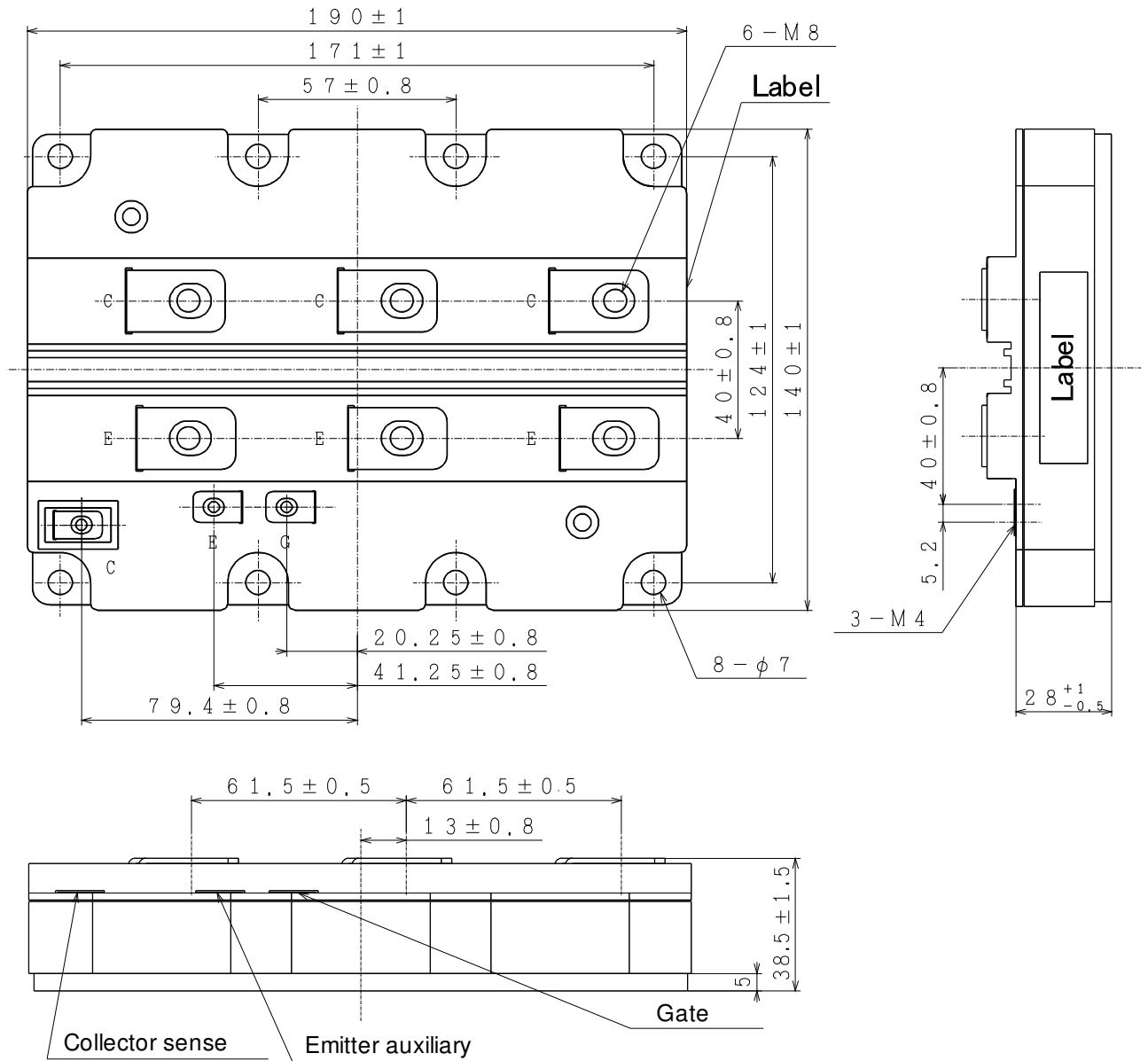


Recovery Loss vs. Gate Resistance

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8. PACKAGE OUTLINE DRAWING

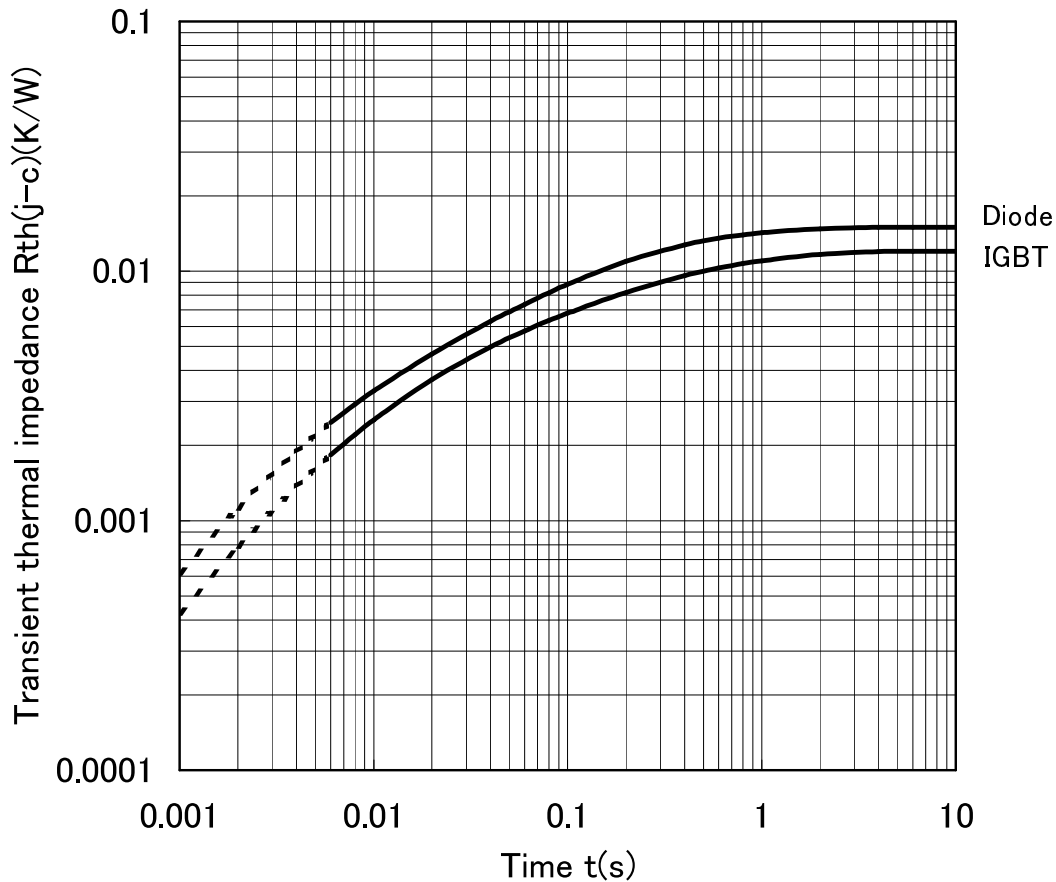


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9. Thermal Impedance

9.1 TRANSIENT THERMAL IMPEDANCE



9.2 Curve approximation model

Following expressions approximates the transient thermal impedance curves.

Please note that the expressions are the curve fitted value, and there is no physical meaning in this expression. The expressions are applicable under following condition only.

Condition 1: Time is more than $t(1)/e$

Condition 2: No heat sink model is considered.

$$Z_{th(j-c)} = \sum z_{th(n)} \cdot [1 - \exp\{-t/\tau_{th(n)}\}] \quad (1)$$

$n(\text{IGBT})$		1	2	3	4	5	6
$Z_{th(n,IGBT)}$ (K/kW)		0.6	1.6	2.1	2.4	2.9	2.4
$\tau_{th(n,IGBT)}$ (s)		0.003	0.01	0.03	0.1	0.3	1
$n(\text{Diode})$		1	2	3	4	5	6
$Z_{th(n,Diode)}$ (K/kW)		1.2	1.6	2	4.4	4.2	1.6
$\tau_{th(n,Diode)}$ (s)		0.003	0.01	0.03	0.1	0.3	1

HITACHI POWER SEMICONDUCTORS

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